Test Number: AS3329\_PGE641\_Checkout

**Test Date**: 07/01/2020

**Last Revised**:

**Last Rev changes:** 

**Discipline**: Land

Test Title: Test of PGE641/DSR-PAR

Test Objective: To do a minor checkout of PGE641/DSR-PAR using data from PGE635/pre-

MAIAC test.

## **Test Time Interval(s):**

2015001 – 2015016 2015105- 2015120

#### **Related Test:**

## **Input DataSet**:

Inputs from AS3299 in LAADS: VNP021KM VNP031KM

Inputs from AS5000 in LAADS:

VNP43MA3

Inputs from AS1 in LAADS:

DFPITI3NXASM

Data for ingest to test archive on mtvs5n1 located in /MODAPSops5/archive/f102/EDG/pge641inputs, which is for input ESDTS: DSRPARDEM, DSRPARLAT, DSRPARLON, DSRPARCLIM.

#### **Recipes/PGEs to be used for SNPP Processing:**

Note: Please use TileSchemeId 345 for this test to limit runs to the 31 tiles previously used in the AS3299 test.

Recipe	Start/End	<u>Version</u>	<u>PGE</u>	PGE Version	PGE Name
	<b>Profile</b>				
VNP_L39m7	1/1	2.0.0	641	2.0.0	DSRPAR

# **Outputs to go to LAADS:**

VNP18A1 VNP18A2 VNP18A1C VNP18A2C